

# A-821PGL / NDA

The NDA version of the A-821PGL offer 16 single-ended or 8 differential 12-bits analog inputs.

## Specifications

### Analog input:

- Channels : 16 single-ended / 8 differential ( by Jumper Setting)
- Resolution : 12-bits
- Conversion rate : 45K/s (max.)
- Input impedance : 10,000M $\Omega$  // 6pF
- Over voltage protection :  $\pm 35V$
- Accuracy : 0.01% of reading  $\pm 1$ bit
- Linearity :  $\pm 1$ bit
- On chip sample & hold
- Zero drift :  $\pm 25$ ppm /  $^{\circ}C$  of FS (max.)
- Programmable gain: 1 / 2 / 4 / 8
- Input range:

GAIN	Input range	Throughput
1	$\pm 5$	45K/s
2	$\pm 2.5$	45K/s
4	$\pm 1.25$	45K/s
8	$\pm 0.625$	45K/s

### Interrupt handling

- A/D Trigger modes: Software trigger, Pacer trigger
- A/D data transfer modes: Polling, Interrupt
- Programmable Pacer Timer  
Pacer timer : 0.00046Hz~0.5MHz (Time based: 2MHz)  
Type : 82C54 / 32-bits (Cascaded pacer timer)
- Interrupt Levels: 2,3,4,5,6,7 Software selectable

### Note :

The NDA version provides A/D function only , the A/D function as same as the A-821PGL, the detail data refer analog input of A-821PGL user's manual.